

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	(integrate\$1 and circuit\$1 and tamper and line and memory and processor\$1 and chip\$1).clm.	US-PGPUB	OR	ON	2008/03/29 18:28
L2	1	(integrate\$1 and circuit\$1 and location \$1 and non-volatile and plurality and secret\$1 and memory and processor\$1 and chip \$1).clm.	US-PGPUB	OR	ON	2008/03/29 18:30
L3	162	tamper\$3 near5 detect\$4 near3 line \$1	US-PGPUB	OR	ON	2008/03/29 18:33
L4	148	I3 and (ic (integrate \$1 near3 circuit\$1))	US-PGPUB	OR	ON	2008/03/29 18:34
L5	136	(tamper\$3 near5 detect\$4 near3 line \$1) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eprom)	US-PGPUB	OR	ON	2008/03/29 18:35
L6	353	(tamper\$3 near5 detect\$4) near8 (memory\$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eprom)	US-PGPUB	OR	ON	2008/03/29 18:37
L7	225	I6 and (ic (integrate \$1 near3 circuit\$1))	US-PGPUB	OR	ON	2008/03/29 18:37
L8	89	7 not 5	US-PGPUB	OR	ON	2008/03/29 18:37
L9	170	tamper\$3 near5 detect\$4 near3 line \$1	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2008/03/29 18:42
L10	102	(tamper\$3 near5 detect\$4 near3 line \$1) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eprom)	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2008/03/29 18:42

L17	286	(tamper\$3 near5 detect\$4) near8 (memory\$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 18:44
L19	282	17 and (ic (integrate \$1 near3 circuit\$1) cpu microprocessor \$1 processor\$1 chip \$1 circuit\$1)	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 18:45
L20	102	10 and (ic (integrate \$1 near3 circuit\$1) cpu microprocessor \$1 processor\$1 chip \$1 circuit\$1)	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 18:45
L22	282	19 not 5	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 18:51
L23	119	22 and (obscure\$4 obfuscate\$4)	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 18:54
L24	331	(obscure\$4 obfuscate\$4) near8 (memory \$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 18:56
L25	1	22 and 24	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 18:57
L26	0	((tamper\$3 intruder\$4 intruder\$3) near5 detect\$4 near3 (circuit\$3 line\$1)) near8 (memory\$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2008/03/29 19:01
L27	157	((tamper\$3 intruder\$4 intruder\$3) near5 detect\$4 near3 (circuit\$3 line\$1)) near8 (memory\$1 nonvolatile\$1 non-volatile\$1 rom ram eeprom eeprom)	USPAT; USOCR; EPO; IBM_TDB	OR	ON	2008/03/29 19:01
L28	156	27 not 5	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 19:01
L29	3976	(obscure\$4 obfuscate\$4) near5 (operation \$1 attempt\$4 access \$3)	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 19:03

L31	4693	(obscur\$4 obfuscat \$4) near8 (operation \$1 attempt\$4 access \$3)	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 19:03
L32	1	28 and 31	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 19:03
L33	55131	(decrypt\$9 decipher \$9 decod\$3 obfuscat \$3 obsc\$9) near6 (operation\$1 attempt \$4 access\$3)	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 19:04
L34	100	28 and 33	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 19:04
L35	89	34 and (ic (integrate \$1 near3 circuit\$1))	USPAT; EPO; IBM_TDB	OR	ON	2008/03/29 19:10

3/29/08 7:29:07 PM

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